Abstract

Adequately assigning features provided by the system to processing units having different architectures incorporated in a system LSI

An analysis unit is provided for counting the number of conditional branch statements and the number of loop control statements, the number of nestings of the conditional branch statements and the number of nestings of the loop control statements, and the number of functions required to generate the conditions of the conditional branch statements and the number of repetitions of loop control statements described in each function of a program describing system features in a high-level language.